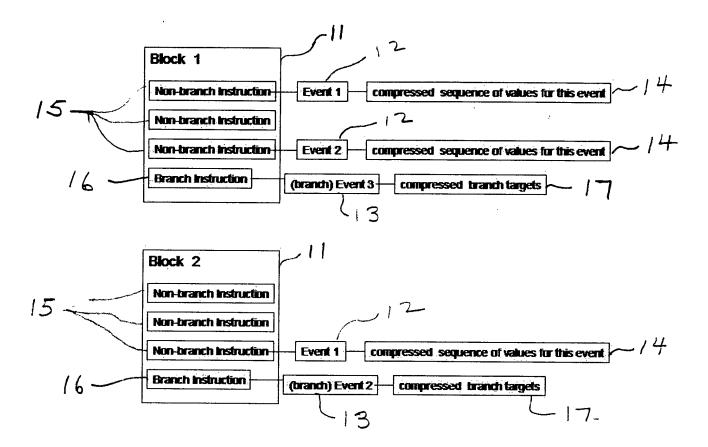
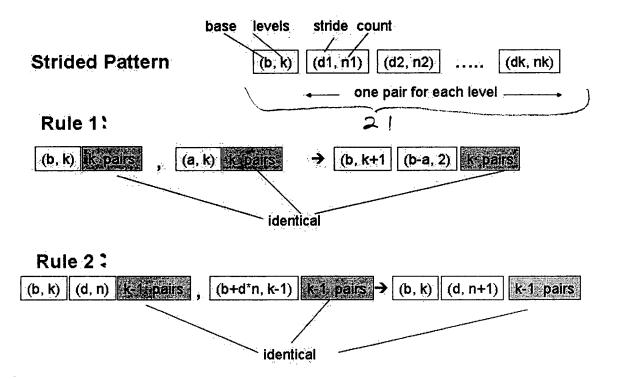
Figure 1 Blocks, Instructions, Events and Compr ssed Value-Sequences



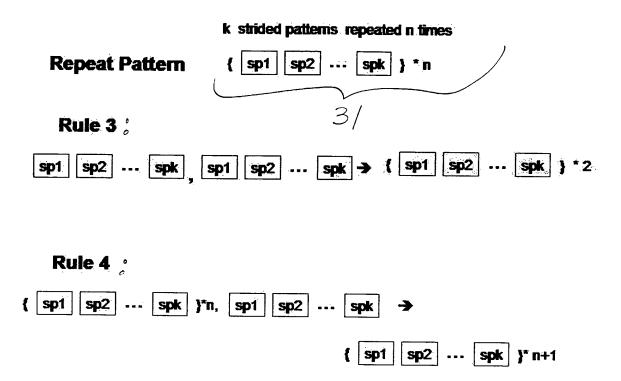
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Figure 2 Stride Pattern Reduction Rules



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Figure 3 R peat Patt rn Reduction Rules



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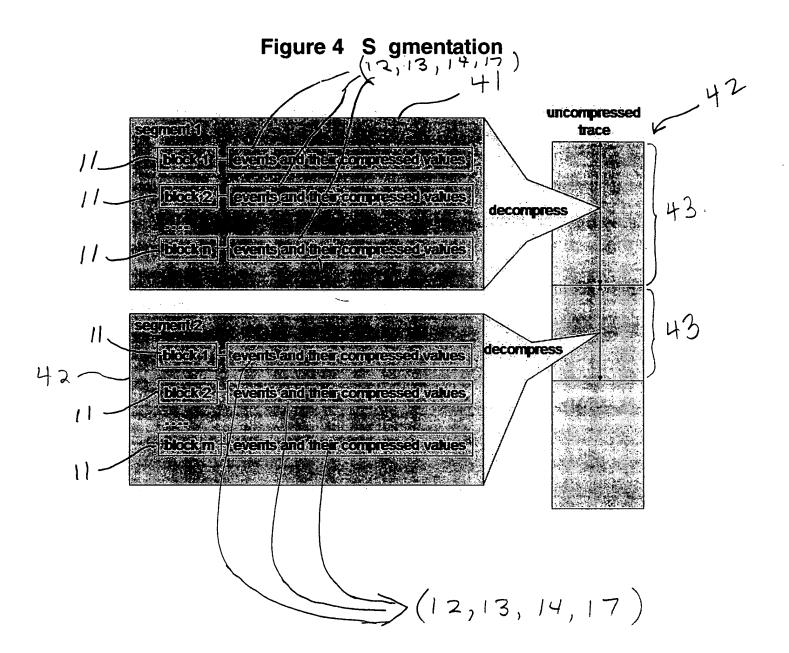
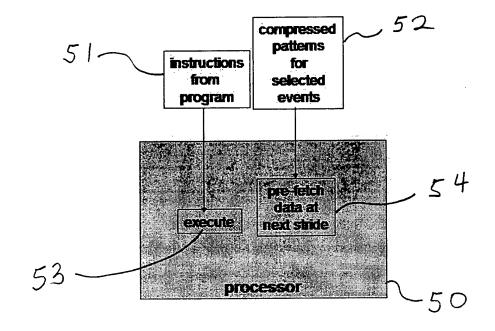


Figure 5 Address Pre-f tching



Vr.Dr. Dr. 1-2,-17-6,